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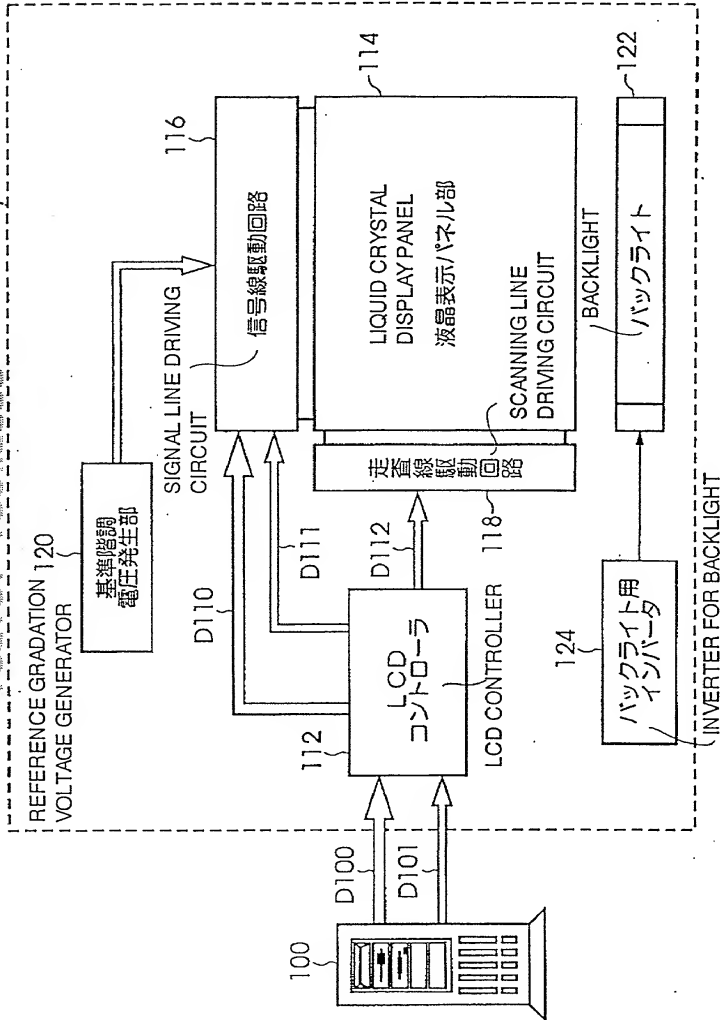
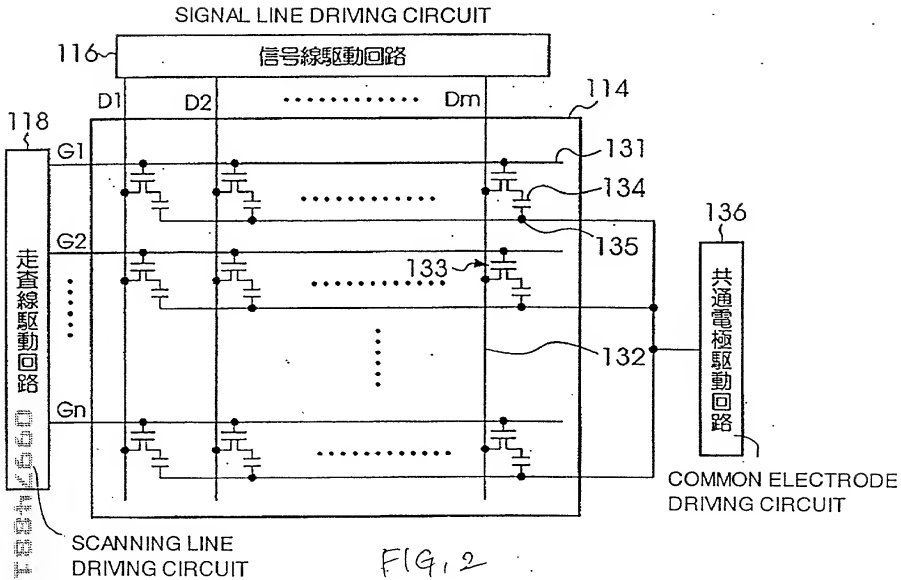
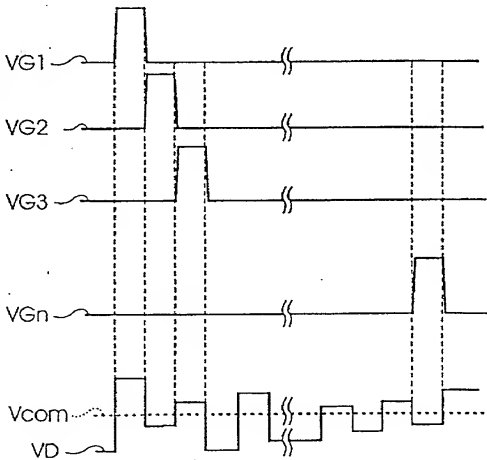


FIG. 1



SCANNING LINE
DRIVING CIRCUIT



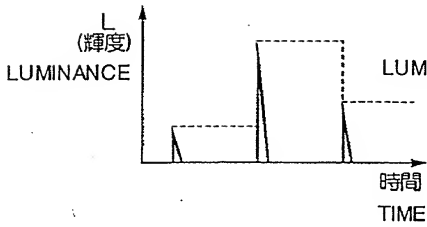


FIG. 4A

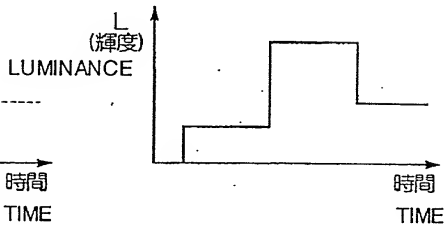


FIG. 4B

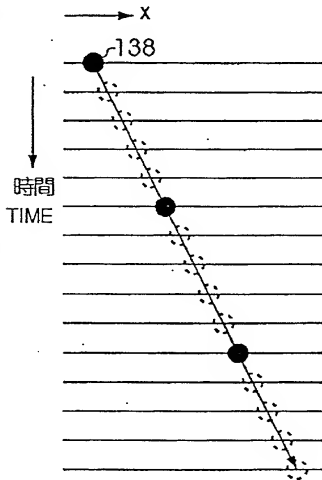


FIG. 5A

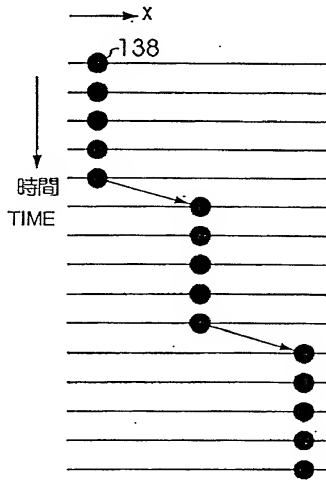


FIG. 5B

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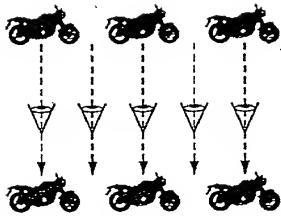


FIG. 6A

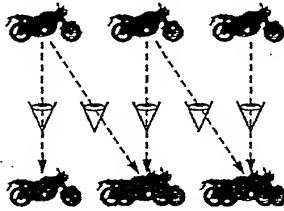


FIG. 6B

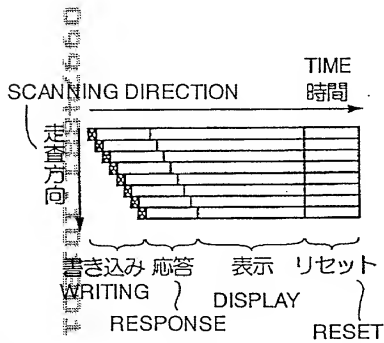


FIG. 7A

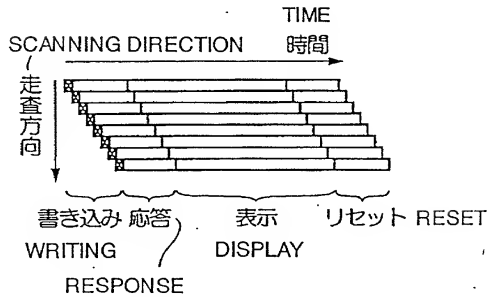


FIG. 7B

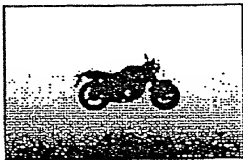


FIG. 7C

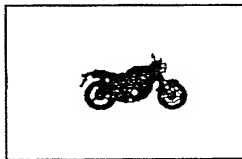


FIG. 7D

SIGNAL LINE DRIVING
 CIRCUIT

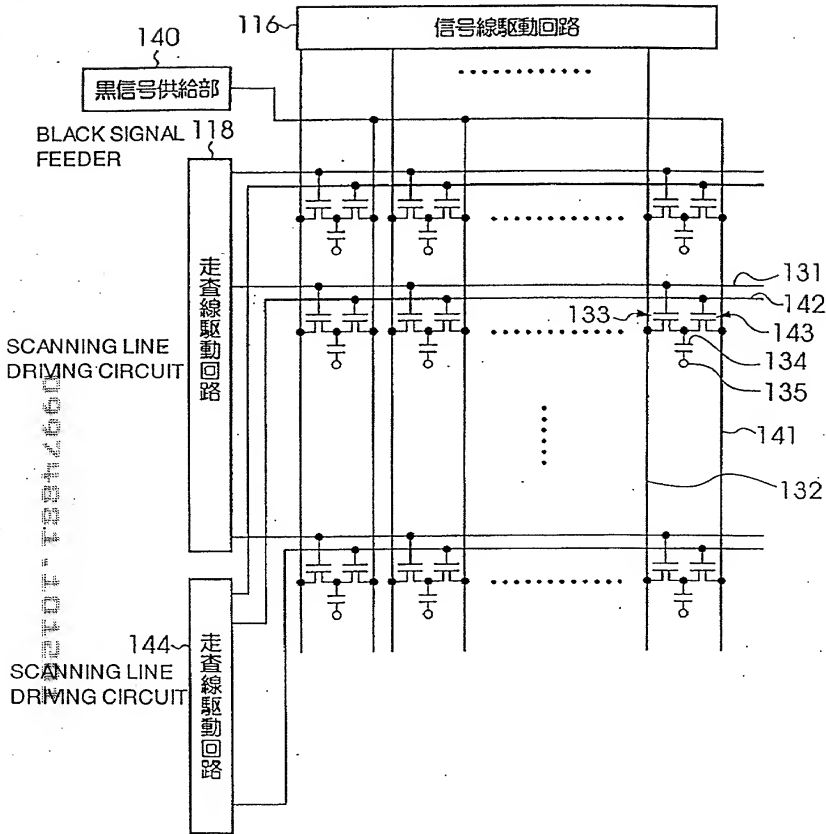


FIG. 8
 PRIOR ART

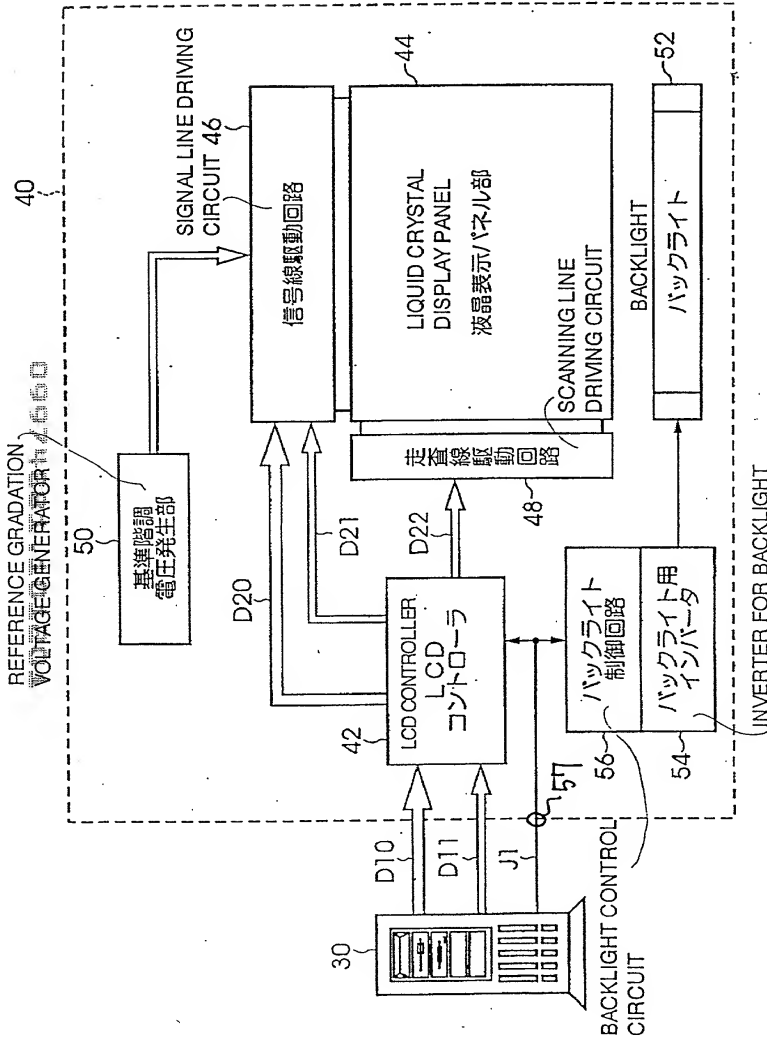


Fig. 9

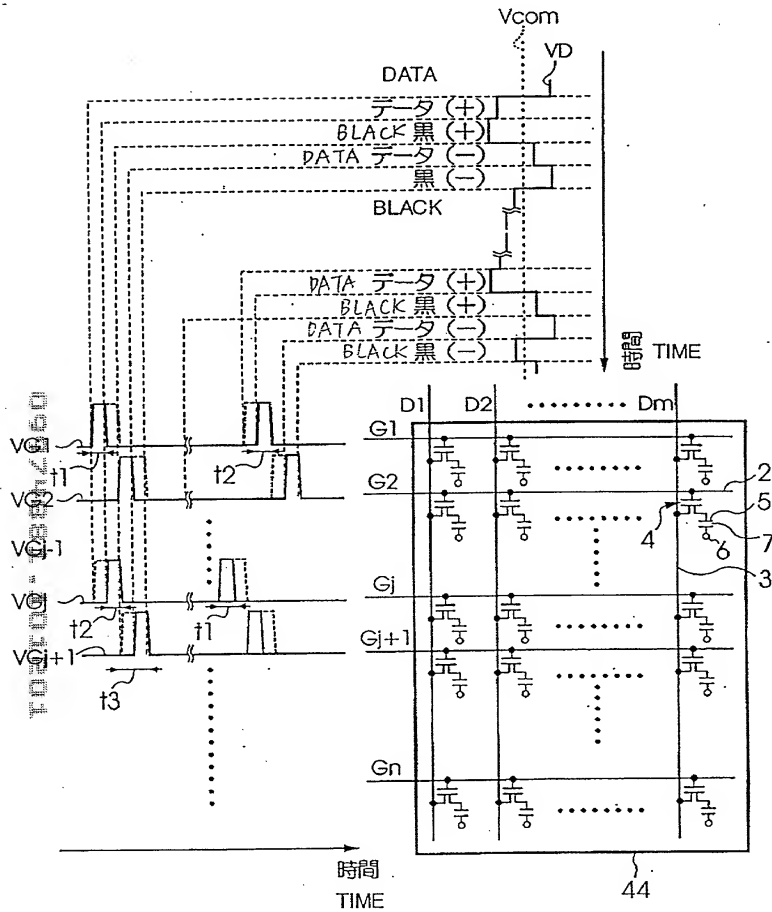


FIG. 10

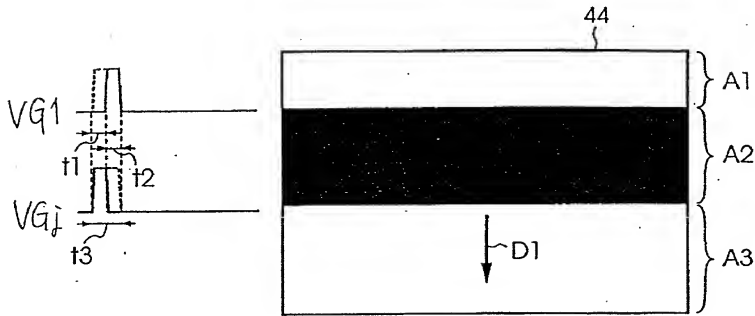
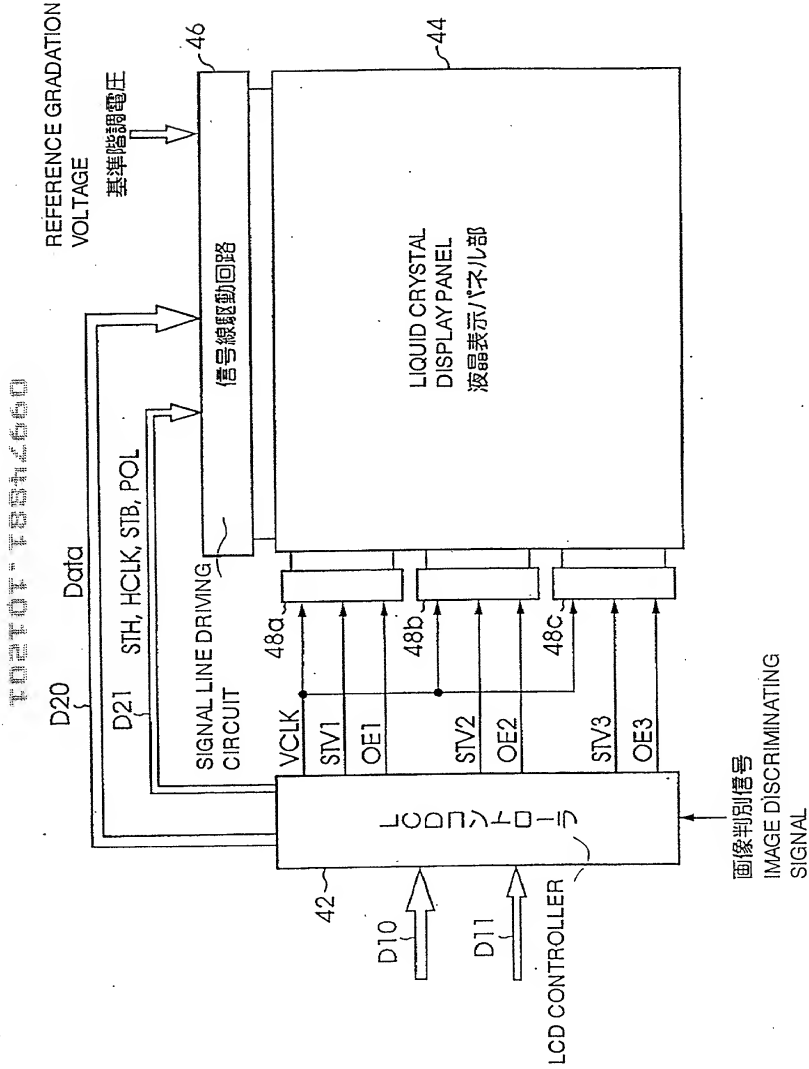


FIG. 11

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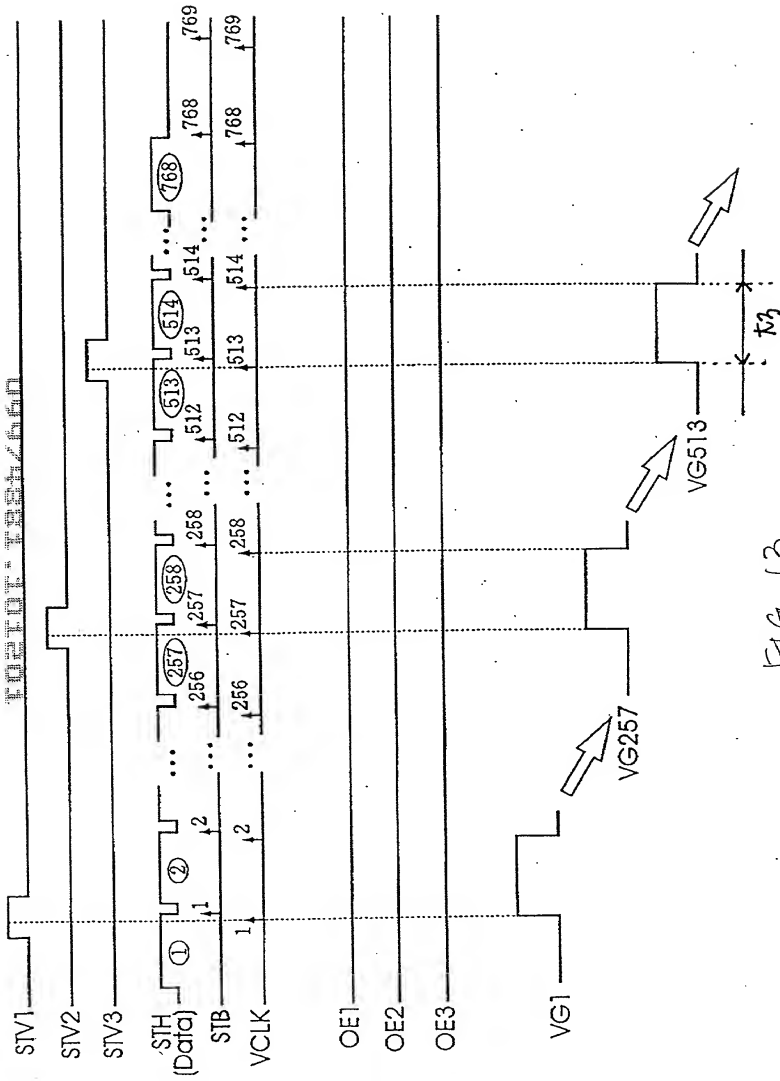


FIG. 13

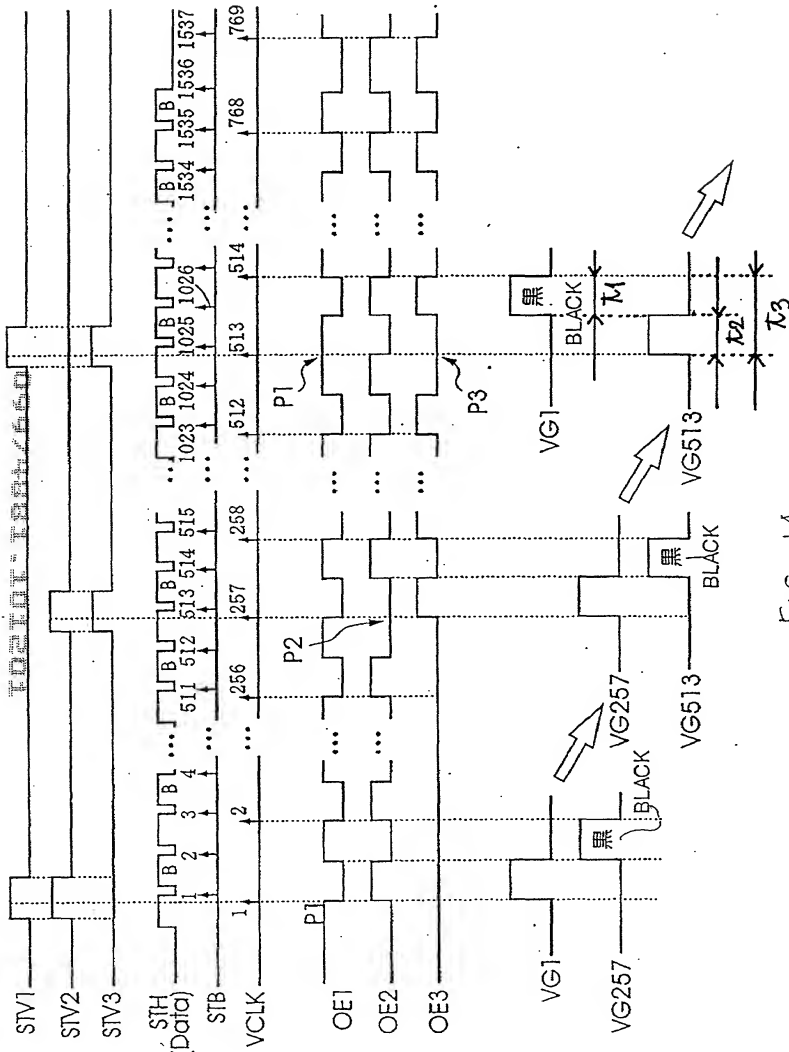
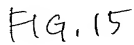


Fig. 14



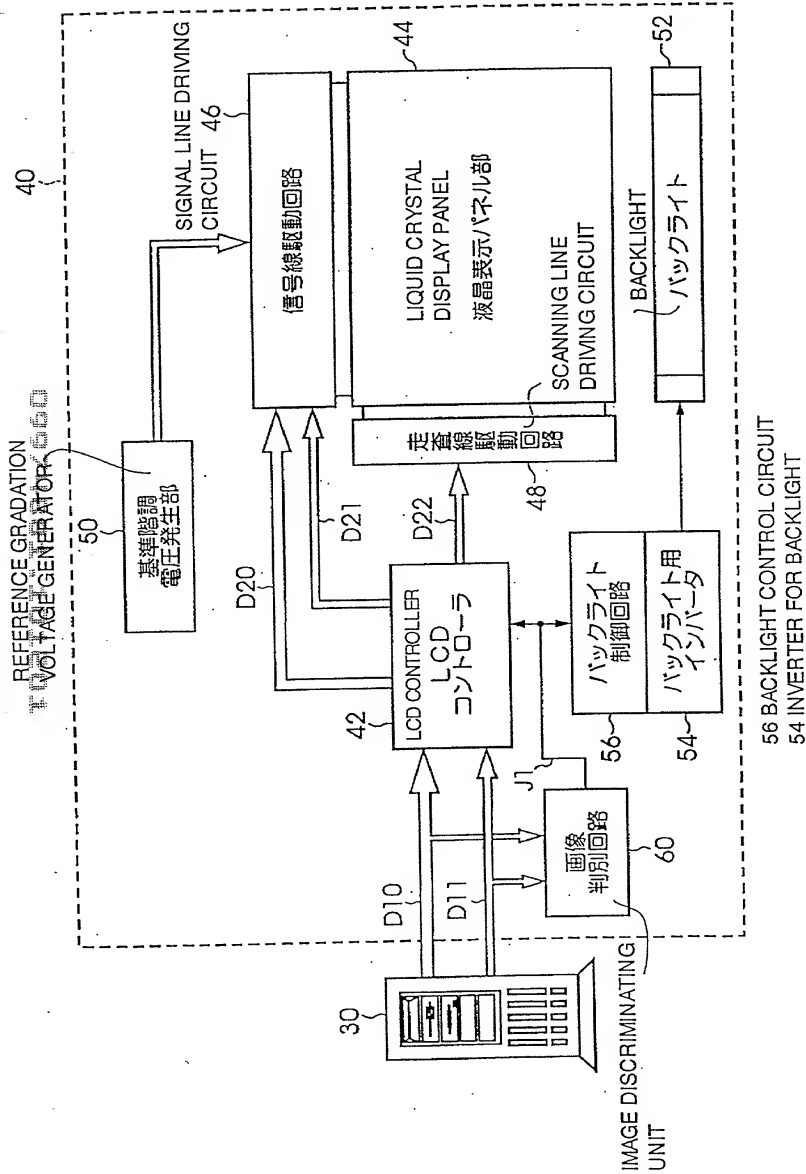


Fig. 16

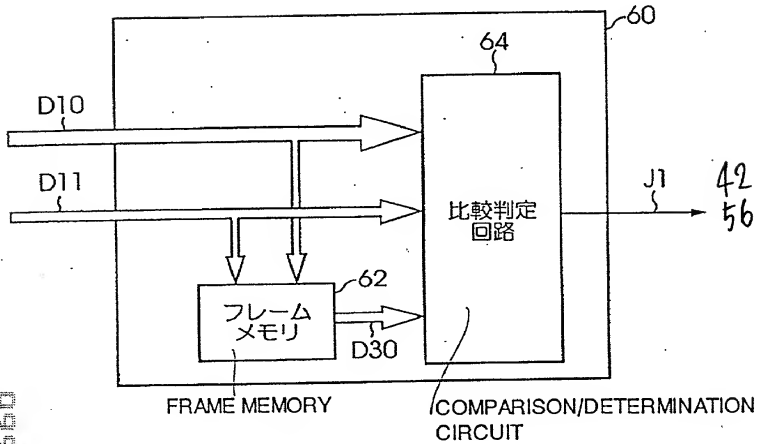


FIG. 17

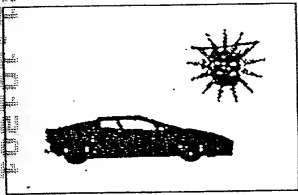


FIG. 18A

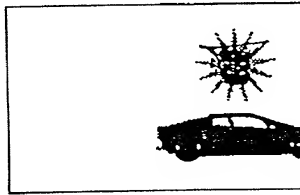


FIG. 18B

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